

COPY

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant(s): SHIRAKAWA, Kazuhiko

Application No.:

Group:

Filed: August 18, 2000

Examiner:

For: HETEROJUNCTION BIPOLAR TRANSISTOR AND METHOD FOR  
FABRICATING THE SAME

LETTER

Assistant Commissioner for Patents  
Box Patent Application  
Washington, D.C. 20231

August 18, 2000  
0020-4741P

Sir:

Under the provisions of 35 USC 119 and 37 CFR 1.55(a), the applicant hereby claims the right of priority based on the following application(s):

<u>Country</u>	<u>Application No.</u>	<u>Filed</u>
JAPAN	11-232671	08/19/99

A certified copy of the above-noted application(s) is(are) attached hereto.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. 1.16 or under 37 C.F.R. 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By: 

DONALD J. DALEY

Reg. No. 34,313

P. O. Box 747

Falls Church, Virginia 22040-0747

Attachment  
(703) 205-8000  
/rem

日本国特許庁

PATENT OFFICE  
JAPANESE GOVERNMENT

Birch, Stepanov  
Kolashchikov  
723100000010  
1001

別紙添付の書類に記載されている事項は下記の出願書類に記載されて  
る事項と同一であることを証明する。

This is to certify that the annexed is a true copy of the following application as filed  
in this Office.

願年月日

Date of Application:

1999年 8月19日

願番号

Application Number:

平成11年特許願第232671号

願人

Applicant(s):

シャープ株式会社

2000年 6月29日

特許庁長官  
Commissioner,  
Patent Office

近藤隆彦

出証番号 出証特2000-3053594